



THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.:	10/803,203	Confirmation No.:	5841
First Named Inventor:	Bulucea, Constantin	Filing Date:	17 March 2004
Patent No.:	7,176,530 B1	Issue Date:	13 February 2007
Group Art Unit:	2826	Examiner:	Quach, T. N.
Atty. Docket No.:	NS-5737 US	(P05737)	
Title:	Configuration and Fabrication of Semiconductor Structure Having N-channel Channel-junction Field-effect Transistor		
Assignee(s):	National Semiconductor Corporation		

Mountain View, California
18 June 2009

**ATTN: CERTIFICATE OF CORRECTION BRANCH
COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450**

Certificate

JUN 24 2009

**REQUEST FOR CERTIFICATE OF CORRECTION
UNDER 35 USC 254 AND 37 CFR 1.322**

Sir:

Patentees' Attorney requests a Certificate of Correction under 35 USC 255 and 37 CFR 1.322 to correct the above patent due to PTO error in printing the patent.

The patent issued with the title "Configuration and Fabrication of Semiconductor Structure Having N-channel Channel-junction Field-effect Transistor". However, the initial words "Configuration and Fabrication of" were deleted from the title via the 37 CFR 1.312 Amendment submitted 8 December 2006.

Various corrections to application Claims 30, 81, 84, 87, 89, and 91 were made via the 8 December 2006 Amendment. These changes appear in patent Claims 36, 52, 15, 20, 24, and 49 respectively corresponding to application Claims 30, 81, 84, 87, 89, and 91. Accordingly, it seems clear that the PTO entered the 8 December 2006 Amendment. In light of the entry of the 8 December 2006 Amendment and the deletion of the initial words "Configuration and Fabrication of" from the title via the 8 December 2006 Amendment, the

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patent should have been printed with the title "Semiconductor Structure Having N-channel Channel-junction Field-effect Transistor".

Additionally, the drawing on the cover page of the patent is not representative of the invention covered by the patent claims. In particular, patent Claim 1 recites a structure containing two n-channel insulated-gate field-effect transistors ("IGFETs"). The two IGFETs recited in patent Claim 1 are an n-channel surface-channel insulated-gate field-effect transistor ("SCIGFET") and an n-channel channel-junction insulated-gate field-effect transistor ("CJIGFET"). However, the drawing on the cover page of the patent repeats patent Fig. 3.1 which illustrates only one n-channel IGFET, namely n-channel SCIGFET 100.

The drawing on the cover page of the patent is also not representative of the patent's title (regardless of the presence or absence of the initial words "Configuration and Fabrication of" in the title). Contrary to the title recitation of an n-channel channel-junction field-effect transistor, n-channel IGFET 100 in Fig. 3.1, i.e., the drawing on the patent's cover page, is an n-channel surface-channel field-effect transistor rather than an n-channel channel-junction field-effect transistor.

Patent Fig. 3.1 is paired with patent Fig. 3.2 which illustrates another n-channel IGFET, namely n-channel CJIGFET 104. N-channel SCIGFET 100 and n-channel CJIGFET 104 respectively shown in Figs. 3.1 and 3.2 respectively implement the n-channel SCIGFET and the n-channel CJIGFET recited in patent Claim 1. N-channel device 104 shown in Fig. 3.2 is also an n-channel channel-junction field-effect transistor as recited in the patent's title. Hence, Figs. 3.1 and 3.2 should appear on the cover page of the patent in order for the drawing on the cover page of the patent to be representative of the claimed invention and the title.

Patentees' Attorney recognizes that the PTO generally prints only one drawing figure on the cover page of a utility U.S. patent such as the above patent. However, Patentees' Attorney is aware that the PTO has occasionally printed two or more drawing figures on the cover page of a utility U.S. patent. In light of this and in light of the fact that the drawing on the cover page of the above patent is not representative of the invention covered by the patent claims or the title, Patentees' Attorney requests that the PTO correct the drawing on the cover page of the above patent to include both of Figs. 3.1 and 3.2.

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As a further point, Patentees' Attorney notes that, aside from the IGFET reference numbers, n-channel SCIGFET 100 and p-channel SCIGFET 102 in Fig. 3.1 are illustrated as respectively having the same structures as n-channel SCIGFET 20 and p-channel SCIGFET 22 in prior art Fig. 1. In other words, the drawing on the cover page of the patent illustrates a prior art structure described in the patent.

The drawing on the cover page of a utility U.S. patent should generally illustrate some feature of the novelty of the claimed invention. Since the drawing on the cover page of the above patent illustrates a prior art structure, the drawing on the cover page of the above patent does not meet the PTO requirement of generally indicating some feature of the novelty of the claimed invention. This is another reason why Fig. 3.1 on the cover page of the above patent is not representative of the invention covered by the patent claims

To the extent that any single drawing figure might be representative of the invention covered by the patent claims, that figure is Fig. 3.2 rather than Fig. 3.1. In the event that the PTO is unwilling to correct the drawing on the cover page of the above patent to include both of Figs. 3.1 and 3.2, Patentees' Attorney alternatively requests that the PTO correct the drawing on the cover page of the above patent by replacing Fig. 3.1 with Fig. 3.2.

Enclosed are two versions of the proposed Certificate of Correction. In the first version which is preferred by Patentees' Attorney, the drawing on the cover page of the above patent is corrected to include both of Figs. 3.1 and 3.2. The drawing on the cover page of the above patent in the second version of the proposed Certificate of Correction is corrected by replacing Fig. 3.1 with Fig. 3.2. Patentees' Attorney requests that the PTO correct the above patent by issuing one of the proposed versions of the Certificate of Correction, preferably the first version.

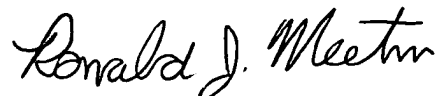
Please direct all inquiries concerning this request to the undersigned attorney at 650-964-9767. This form is submitted in duplicate.

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Respectfully submitted,



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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

Page 1 of 1

PATENT NO. : 7,176,730B1

APPLICATION NO. : 10/803,203

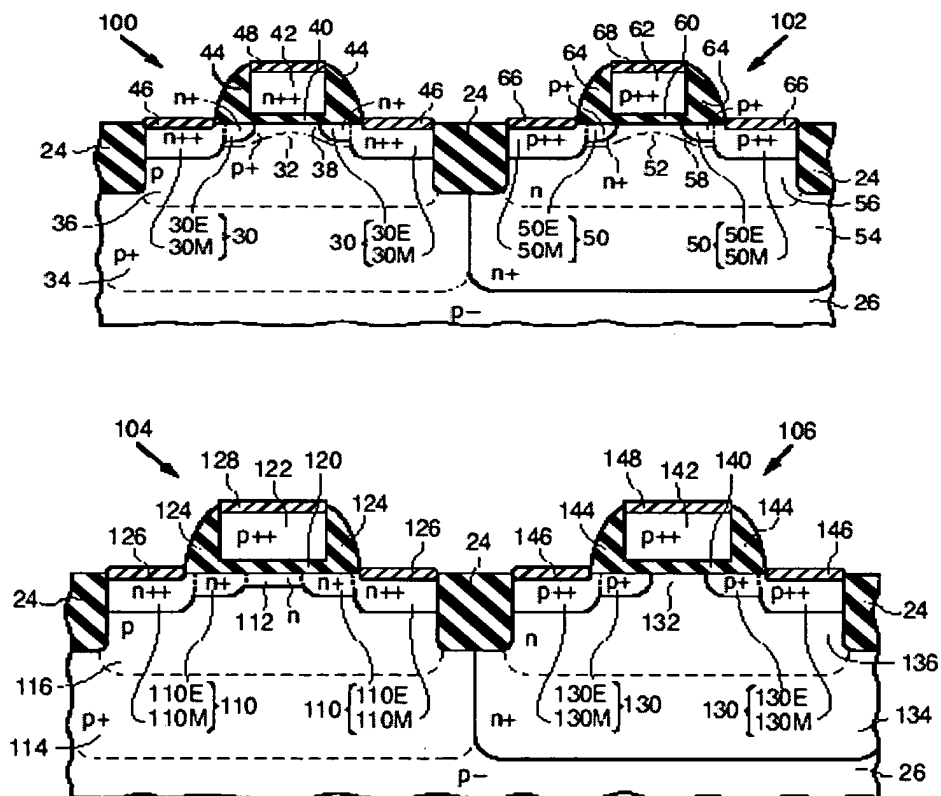
ISSUE DATE : Feb. 13, 2007

INVENTOR(S) : Bulucea et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

The title should read "SEMICONDUCTOR STRUCTURE HAVING N-CANNEL CHANNEL-JUNCTION FIELD-EFFECT TRANSISTOR".

The drawing on the cover page should be:



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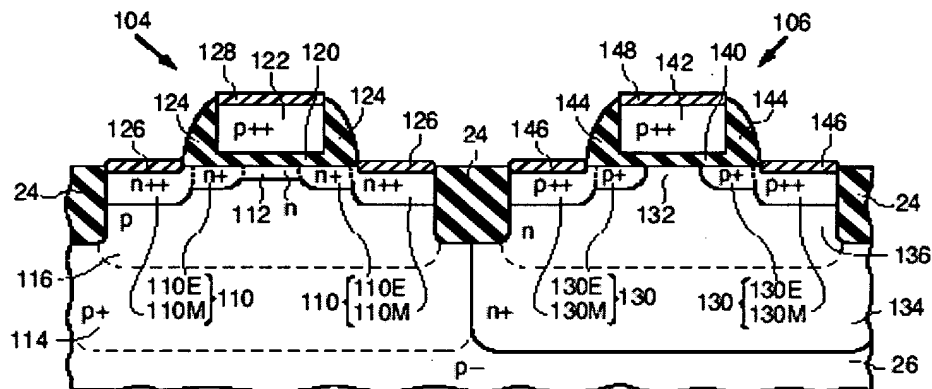
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